

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: David M. Fried, et al

Examiner: Cuong Quang Nguyen

Serial No.:

Art Group Unit: 2811

Filed: Herewith

Title: FINFET CMOS WITH NVRAM CAPABILITY

Commissioner for Patents  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

In accordance with 37 C.F.R. §1.56, 1.97 and 1.98, it is respectfully requested that the following references, which are also listed on the attached form PTO-1449, be made of record in the above-identified patent application.

- cn*1. U.S. Patent No. 6,040,218, issued 3/2000 to Lam
- cn*2. U.S. Patent No. 6,107,141, issued 8/2000 to Hsu et al.
- cn*3. U.S. Patent No. 6,108,239, issued 8/2000 to Sekariapuram et al.
- cn*4. U.S. Patent No. 6,114,725, issued 9/2000 to Furukawa et al.
- cn*5. U.S. Patent No. 6,143,636, issued 11/2000 to Forbes, et al.
- cn*6. U.S. Patent No. 6,133,098, issued 10/2000 to Ogura, et al.
- cn*7. U.S. Patent No. 6,174,771, issued 01/2001 to Leu
- cn*8. U.S. Patent No. 6,198,125, issued 03/2001 to Yamazaki, et al.
- cn*9. U.S. Patent No. 5,256,584, issued 10/1993 to Hartmann
- cn*10. U.S. Patent No. 5,455,792, issued 10/1995 to Yi
- cn*11. U.S. Patent No. 5,468,663, issued 11/1995 to Bertin, et al.
- cn*12. U.S. Patent No. 5,739,567, issued 04/1998 to Wong
- cn*13. U.S. Patent No. 5,041,886, issued 08/1991 to Lee
- cn*14. U.S. Patent No. 6,033,957, issued 03/2000 to Burns, Jr. et al.
- cn*15. U.S. Patent No. 6,157,061 issued 12/2000 to Kawata
- cn*16. Japanese Patent No. JP408162547A, issued 06/1996

Pursuant to 37 C.F.R. §1.98(d), copies of the references listed on the enclosed Form PTO-1449 are not provided since they were previously made of record in parent application Serial No. 10,063,095 filed February 19, 2002. The references were cited in the Information Disclosure Statement filed concurrently with the application on February 19, 2002 and on Form

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08-18-04